

P é t e r J ó z s e f L ó d r i → Preferred Networks (PFN · プリファードネットワークス)

Role: Senior Rust Systems & Custom AI Accelerator Engineer (MN-Core / PLaMo / Rustuna) · Comp: ¥12,000,000 – ¥22,000,000+ JPY · Location: Otemachi, Chiyoda-ku, Tokyo (大手町フィナンシャルシティ)
Dedicated Subdomain: <https://pfn.vaked.dev> · Portal: <https://www.preferred.jp/en/careers/> (careers@preferred.jp)

01. COVER LETTER & OUTREACH MESSAGE (INMAIL / EMAIL)

Dear Preferred Networks (PFN) Engineering & Talent Team,

I created a dedicated interactive systems portfolio and bilingual application dossier for Preferred Networks in Otemachi, Tokyo:

- Live Interactive Portfolio (MN-Core Systolic Array Simulation): <https://pfn.vaked.dev>
- Formal 履歴書 + 職務経歴書 (PDF): <https://pfn.vaked.dev/resume.pdf>
- Systems Portfolio (PDF): <https://pfn.vaked.dev/portfolio.pdf>
- Full Technical Application Note: <https://pfn.vaked.dev/application>

Why PFN is my ideal technical home in Tokyo:

- Bit-Exact C/Rust Low-Bit Runtimes: I built pureQTern and project-zero — dependency-free 1.58-bit ternary codecs in C and Rust verified to the bit by golden SHA-256 hashes, plus custom Metal GPU kernels (MLX-QUANT) achieving $12.80\times$ compression.
- High-Throughput Systems & Open Source: Core Architect on Osaurus (osaurus-ai/osaurus) & Osiris (simplifaisoul/osiris), contributor to Headroom (PR #1418), and 9+ years leading high-concurrency Go/Rust/Python systems (ASAPP, DailyWire).

Warm regards,

P é t e r J ó z s e f L ó d r i

<https://www.linkedin.com/in/peter-lodri/>

02. 1-CLICK LINKEDIN CONNECTION NOTE (< 300 CHARS)

Hi! I built a live interactive MN-Core systolic array & Rust systems portfolio specifically for Preferred Networks in Otemachi: <https://pfn.vaked.dev> — showcasing bit-exact C/Rust 1.58-bit ternary codecs (pureQTern) & custom GPU kernels. Would love to connect! — P é t e r

03. LINKEDIN COMPANY & LEADERSHIP CONTACTS

- Company LinkedIn: <https://www.linkedin.com/company/preferred-networks/>
- Company Jobs: <https://www.linkedin.com/company/preferred-networks/jobs/>
- Company People: <https://www.linkedin.com/company/preferred-networks/people/>
- Recruiter Search: <https://www.linkedin.com/search/results/people/?keywords=Preferred%20Networks%20%28Recruiter%20OR%20Talent%20OR%20HR%20OR%20MN-Core%20OR%20PLaMo%29>
- Toru Nishikawa (西川徹) (Co-Founder, President & CEO, Preferred Networks):
<https://www.linkedin.com/search/results/people/?keywords=Toru%20Nishikawa%20Preferred%20Networks>
Co-founder leading PFN's vertically integrated computing stack (MN-Core, PLaMo, Matlantis).
- Daisuke Okanohara (岡野原大輔) (Co-Founder & Chief Technology Officer (CTO), Preferred Networks):
<https://www.linkedin.com/search/results/people/?keywords=Daisuke%20Okanohara%20Preferred%20Networks>
Co-founder & CTO pioneering deep learning infrastructure, low-bit inference, and PLaMo LLMs.
- PFN Talent Acquisition & Compiler/Systems Team (Engineering Recruiting · Otemachi, Tokyo): <https://www.linkedin.com/search/results/people/?keywords=Preferred%20Networks%20Recruiter%20OR%20Talent%20Tokyo>
Technical recruiting team for MN-Core runtime, Optuna/Rustuna, and PLaMo inference.

04. BILINGUAL MOTIVATION / 志望動機

Preferred Networks stands virtually alone in Japan in owning the entire vertical stack — from custom MN-Core systolic matrix silicon and compiler toolchains down to sovereign Japanese foundation models (PLaMo) and black-box optimization (Optuna / Rustuna). My engineering obsession is that exact boundary where mathematical structure meets hardware memory layout: writing zero-dependency C and Rust ternary codecs (pureQTern) that agree to the single bit, and custom GPU kernels (MLX-QUANT) that cut memory bandwidth by $12.80\times$.

Preferred

Networks (PFN) が推進する独自AI半導体「MN-Core」シリーズから国産基盤モデル「PLaMo」、最適化基盤「Optuna / Rustuna」に至る垂直統合技術は、世界のAI計算基盤の中でも他に類を見ないものです。C言語とRustで1ビットの狂いもなく一致する三値量子化コーデック (pureQTern) やカスタムGPUカーネル (MLX-QUANT) をゼロ依存で実装してきた私のシステム工学スキルを、大手町のPFNチームでMN-CoreランタイムおよびPLaMo推論基盤の高速化に捧げたいと強く志望しています。