

Péter József Lódrí

Senior Rust Systems & Custom AI Accelerator Engineer (MN-Core / PLaMo / Rustuna) — Vertical integration from custom matrix silicon to bit-exact Rust codecs and foundation models.

Live Interactive Showcase: <https://pfn.vaked.dev> · LinkedIn: <https://www.linkedin.com/in/peter-lodri/>

01. SELECTED SYSTEMS & INTERACTIVE WORKS FOR Preferred Networks (PFN · プリファードネットワークス)

pureQTern & project-zero [C ↔ Rust · Bit-Exact Golden Hash] — <https://github.com/peterlodri-sec>
Zero-dependency 1.58-bit ternary codec in both C and Rust agreeing to the exact bit on a golden SHA-256 hash, paired with a bare-metal C inference server.

8b-is / MLX-QUANT [Metal GPU Kernels · 12.80× Compression] — <https://github.com/8b-is>
Custom low-bit ternary quantization and packed matrix-vector kernels eliminating memory-bandwidth bottlenecks for foundation model inference.

Headroom (PR #1418 — kompress) [OSS Contributor · Algorithmic Routing] — <https://github.com/headroomlabs-ai/headroom/pull/1418>
Per-content-type bias multipliers for domain-aware compression routing in Headroom.

Osaurus & Osiris [OSS Architect · Autonomous Runtime] — <https://github.com/osaurus-ai/osaurus>
Core Architect on Osaurus (native Swift agent runtime) and Osiris (real-time OSINT intelligence engine).

deepseek-go [Go · LLM Systems SDK] — <https://github.com/peterlodri-sec>
High-performance, low-allocation Go client and streaming runtime for foundation model APIs.

ZENO (銭 · 禅 · 善) [Deterministic Ledger · Rust/TS] — <https://zeno.vaked.dev>
Cryptographic double-entry Kintsugi SHA-256 ledger and high-precision settlement engine.

02. ALIGNMENT WITH YOUR ENGINEERING ROADMAP / 志望動機

Preferred Networks stands virtually alone in Japan in owning the entire vertical stack — from custom MN-Core systolic matrix silicon and compiler toolchains down to sovereign Japanese foundation models (PLaMo) and black-box optimization (Optuna / Rustuna). My engineering obsession is that exact boundary where mathematical structure meets hardware memory layout: writing zero-dependency C and Rust ternary codecs (pureQTern) that agree to the single bit, and custom GPU kernels (MLX-QUANT) that cut memory bandwidth by 12.80×.

Preferred

Networks (PFN) が推進する独自AI半導体「MN-Core」シリーズから国産基盤モデル「PLaMo」、最適化基盤「Optuna / Rustuna」に至る垂直統合技術は、世界のAI計算基盤の中でも他に類を見ないものです。C言語とRustで1ビットの狂いもなく一致する三値量子化コーデック (pureQTern) やカスタムGPUカーネル (MLX-QUANT) をゼロ依存で実装してきた私のシステム工学スキルを、大手町のPFNチームでMN-CoreランタイムおよびPLaMo推論基盤の高速化に捧げたいと強く志望しています。

03. DIRECT LINKS & LINKEDIN CONTACTS

- Toru Nishikawa (西川徹) (Co-Founder, President & CEO, Preferred Networks): <https://www.linkedin.com/search/results/people/?keywords=Toru%20Nishikawa%20Preferred%20Networks> — Co-founder leading PFN's vertically integrated computing stack (MN-Core, PLaMo, Matlantis).
- Daisuke Okanohara (岡野原大輔) (Co-Founder & Chief Technology Officer (CTO), Preferred Networks): <https://www.linkedin.com/search/results/people/?keywords=Daisuke%20Okanohara%20Preferred%20Networks> — Co-founder & CTO pioneering deep learning infrastructure, low-bit inference, and PLaMo LLMs.
- PFN Talent Acquisition & Compiler/Systems Team (Engineering Recruiting · Otemachi, Tokyo): <https://www.linkedin.com/search/results/people/?keywords=Preferred%20Networks%20Recruiter%20OR%20Talent%20Tokyo> — Technical recruiting team

for MN-Core runtime, Optuna/Rustuna, and PLaMo inference.